

## Pipelined Adc Design Enhancement Techniques

**accuracy enhancement techniques in low-voltage high-speed ...** - accuracy enhancement techniques in low-voltage high-speed pipelined adc design by jipeng li a dissertation submitted to oregon state university **basic block of pipelined adc design requirements** - basic block of pipelined adc design requirements vilem kledrowetz, jiri haze dept. of microelectronics, brno university of technology, technicka 3058/10, 616 00 brno, czech republic **analog circuits and signal processing** - pipelined adc design and enhancement techniques consulting editor: mohammed ismail. ohio state university **pipelined addc enhancement tech niques** - pipelined adc enhancement techniques ... abstract in this work three techniques to improve pipelined adc performance with respect to linearity and power consumption are presented. the first technique enables rapid background digital correction of both dac and gain errors in the multi-bit first stage of an 11-bit pipelined adc. the proposed rapid calibration scheme enables significantly shorter ... **mt-024: adc architectures v: pipelined subranging adcs** - the pipelined adc had its origins in the subranging architecture which was first used in the 1950s as a means to reduce the component count and power in tunnel diode and vacuum tube flash adcs (see references 1, 2). **pipelined adc design - a tutorial - ampic lab** - pipelined adc design - a tutorial - based on slides from dr. bibhudatta sahuo university of illinois at urbana-champaign. slides by bibhudatta sahuo-22 outline review of pipelined adcs impact of scaling on data converter design why calibration? basics of digital calibration techniques survey of digital calibration techniques conclusion. slides by bibhudatta sahuo-3 introduction data converter ... **reliability enhancement of pipelined adc - researchgate** - reliability enhancement of pipelined adc ... mation analog-to-digital converter (psac) is the combination of the advan- tages of both beneficial concepts. the psac has lower power consumption as a ... **analog circuits and signal processing - link.springer** - pipelined adc design and enhancement techniques consulting editor: mohammed ismail. ohio state university

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